

As per our telephonic conversations earlier in the day, I am writing about the **"Recruitment Drive 2018"** proposal for our prestigious organization Synapse Techno Design Innovations Pvt. Ltd. Please find the details mentioned below.

ABOUT SYNAPSE

Synapse Design is an industry leader in design services and is the engineering backbone of most top tier Semiconductor and System companies around the world. Synapse Design target customers are companies with \$1 billion in revenue, and we enable them to meet their technical & resource challenges to build the next generation products. Founded in 2003, headquartered in San Jose (Silicon Valley) with operations in Irvine, Boston, San Diego, Boulder-Colorado, China, Europe and India. Synapse Design has approximately 1000 employees distributed around the globe and is aggressively growing. Synapse Design assists its clients in developing their next generation flagship product lines (mobile devices, complex routers/switches, consumer products, storage, microprocessor, graphics processors etc.). This includes developing cutting edge technologies that are key and rare in the industry. Experts in building the most complex SoC's, Synapse Design further differentiates by having one of the strongest analog/mixed signal & software design teams in the industry. The Synapse Design engagement model is equally diverse - all the way from architecture or product spec development to full turnkey projects, or deploying large/small teams around the world to help its clients deliver their latest product - fastest to market.

For further details about the company, please visit our website www.synapse-da.com.

ELIGIBILITY CRITERIA

10th	60% and above
12th	60% and above
B. Tech	70% and above or 7.5 CGPA
Batch	2016 and 2017 passed out
Stream	ECE, ETC, EEE, EE, CSE, AIE and EIE
Soft skills	Decent communication skills and professional presentable outlook
Professional skills	Willingness to work in VLSI
Commitment to company	3 years minimum

STEPS FOR SELECTION PROCEDURE

Round one : Written Test (At Ranchi/ Bangalore)

Round two : Technical Interview (Telephonic)

Round three : Personal Interview (Skype)

Syllabus for written examination - Digital Electronics, Analog Electronics, Basic Electrical, Circuit Theory, C & C++ Programming, and Basic VLSI. There will be a combination of subjective and objective questions.

LAST DATE

Last date for submission of profiles is **14th April 2018 (Saturday)**.

POINT TO REMEMBER

- The candidates should mention the preferred location (Bangalore/ Ranchi) for appearing in the written exam for which the dates shall be communicated to them through Emails.
- Please send in your profiles to us at aamir.junaid@yahoo.com
Please be informed that we at Synapse Design take immense pride in what we do and deliver to our customers and always work towards the betterment of the company and its employees and hence be rest assured that joining this organization would assure all the candidates of a very bright and prosperous future.

**Shimoni Prasad
HR and Admin Executive
Synapse Techno Design Innovations Pvt. Ltd.**